Srikanth Devapooja

4-1-121/22/B, V.S.T Colony, Nacharam, Hyderabad -500076.

[srikanth.devapooja.sri@gmail.com](mailto:srikanth.devapooja.sri@gmail.com) 9177444270

**Career Objective**

* Innovative Engineer with extensive experience in Electronics and Telecommunication products.
* Now looking for a challenging & responsible position to enhance my skills, willing to work in an environment that offers growth and exposure to the latest technologies where I can excel and realize my potential.

**Career Summary**

* Working as a Engineer supporting the design, development and quality electronic products.
* Knowledge on various protocols such as RS232/485, I2C, SPI, and TCP/IP.
* Familiar to lab instruments like Multimeter, Oscilloscope, OPM, LCR Meter, Logic analyzer, Optical Spectrum analyzer, DSO, 1G/10G optical fiber Tester, DCA etc & good soldering, wiring skills.
* Good experiences in Performing Regression Testing to verify and uncover errors.
* Experienced on testing, troubleshooting of electronic boards via circuit diagram.
* Experience on connecting to electronic boards using Linux platform.
* Working on both Client and Academic Projects in Embedded Domain.
* Validation of embedded Telecom products and Involved in Black Box testing, Bench level Testing of Telecom modules.
* Knowledge/Exposure to Hardware (Circuit Schematic, PCB).
* Experienced of testing FTTH, GPON related products.
* Good Experience on programming languages & understanding on embedded and networking concepts.
* Good knowledge in reading schematics, ability to do debug and isolate failures.
* Self-starting/self-motivating individual, able to plan and handle multiple activities to accomplish goals.

**Skills**

* Operating Systems : Windows Family, Linux.
* Designing Tools : ORCAD 10.5, KiCAD and Proteus.
* Editors/Compilers : Keiluvision4, Eclipse.
* Programming languages : C, Python
* Documentation Tools : MS-Office.
* Other languages : Java, HTML, Software Automation Tools (QTP, Selenium)

**Experience**

Engineer, May 2017 – Till Date

**Arunodaya Electro Power Technologies** – Hyderabad, Ecil

* Working on engineering and client projects.
* Basic knowledge on Hardware.
* Knowledge on 8/16/32 bit microcontrollers. Interfacing of Input and Output devices.
* Knowledge of C embedded C, Assembly programming.
* Willingness to co-ordinate with other engineering teams, PCB Layout, Validation, and Production and bring about constructive solutions.

Engineer – R&D, Jan 2015 – May 2017

**United Telecoms Limited** – Hyderabad, Ecil

Project Experience:

Title: DWDM Transmitter & Receiver Modules

Role: Engineer – R&D

Team Size: 2

Responsibilities**:**

* Bench level Testing of Transmitter and Receiver optical modules with 10G DWDM Line-cards.
* Programming and monitoring the output signals & knowledge in Electronic hardware, networking concepts.
* Testing, Inspection of electronic components & Assembling Electronic components.
* Configuring Lattice FPGA program with ispVM via JTAG Port of DWDM System.
* Designing of circuits using ORCAD, KICAD.
* Tested on Transmitter Modules with 10G supported DWDM Line-cards according to Testing levels like BER(Bit error ratio) testing using DCA & Wavelength monitoring using Optical Spectrum analyzer (OSA) testing Equipment.
* Controlling and optimizing work and workloads in a production process.
* Handling the tasks & preparing testing documentation for future reference.
* Calibration of all equipment as per requirement and jig.

Title: Gigabit Passive Optical Network (GPON)

Role: Engineer-R&D

Client: BSNL

Team Size: 5

Responsibilities:

* Installation & Configuration settings with different services.
* Tested 1G & 10G data using JDSU test equipments.
* Capturing the Data from Line-cards & SCC (Switch Control Card) using Linux Platform.
* Tested on voice, video & data services.
* Manual testing of Software Application for provision of services to users.
* Has been involved in creating test scenario, test cases & Execution of test cases.
* Installation of GPON system with ONT, Line-cards, Testing equipments, executing test program software.

**Education**

|  |  |  |  |
| --- | --- | --- | --- |
| **Qualification** | **College/School** | **Year** | **Percentage** |
| B.Tech (ECE) | Holy Mary Institute of Technology and Science | 2014 | 69.25 |
| Intermediate | Sri Chaitanya Junior Kalasala | 2010 | 77.77 |
| SSC | Sandeepam Vidyalaya High School | 2008 | 63.16 |